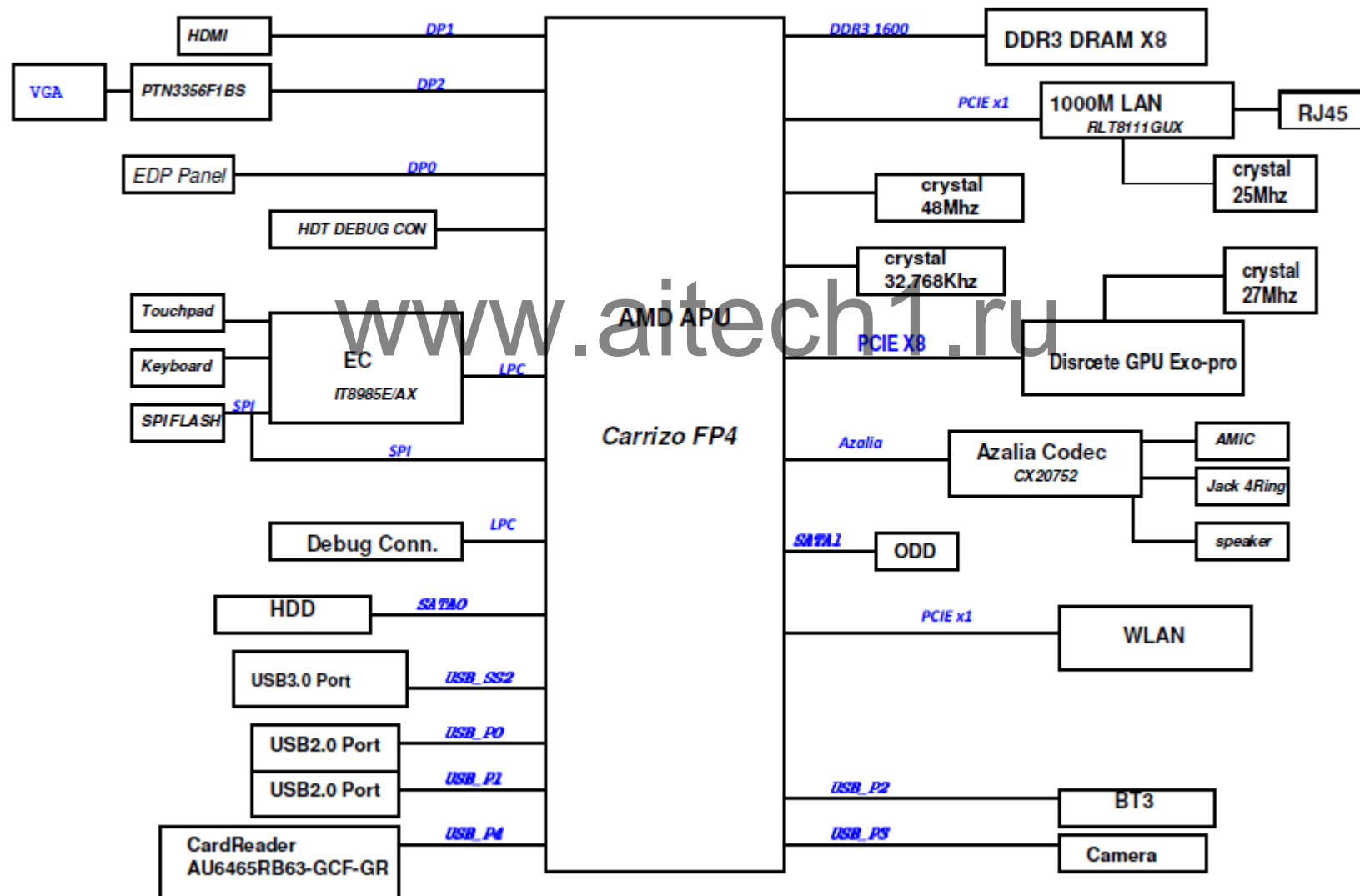
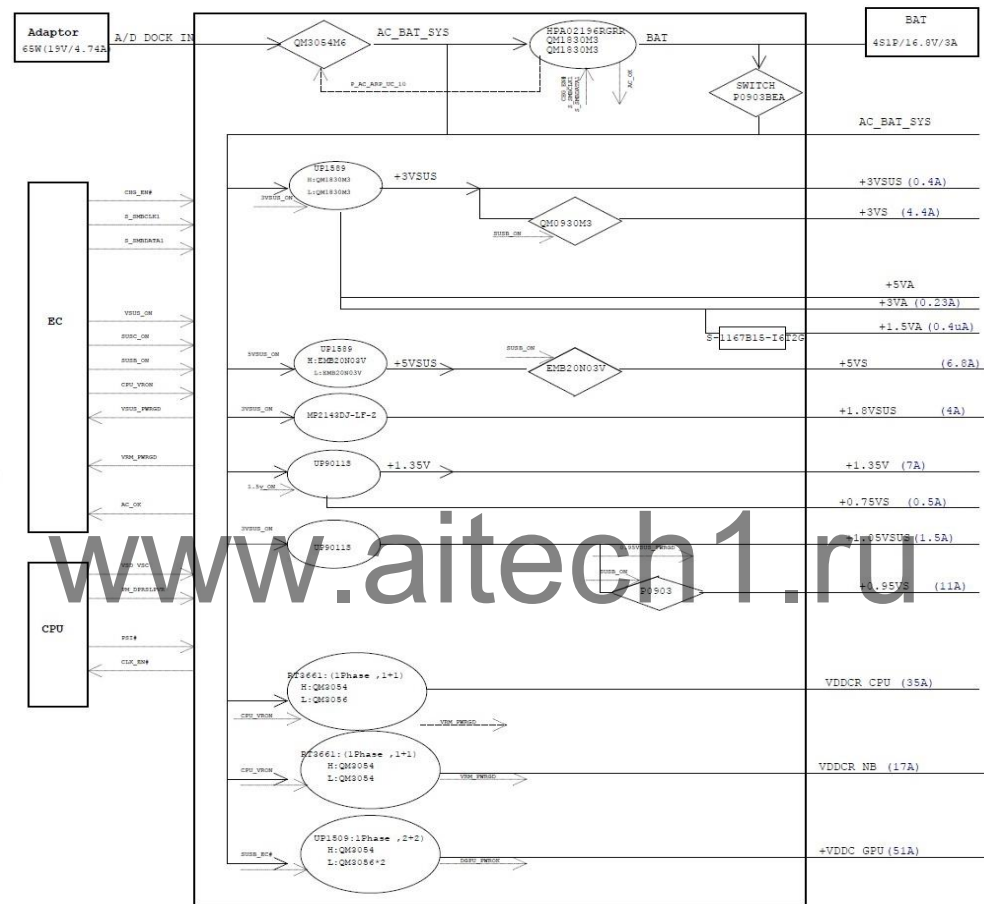


BLOCK DIAGRAM

X455DG Schematic



POWER FLOW



GA20 → A20GATE → GA20IN

FP4_SUS4#

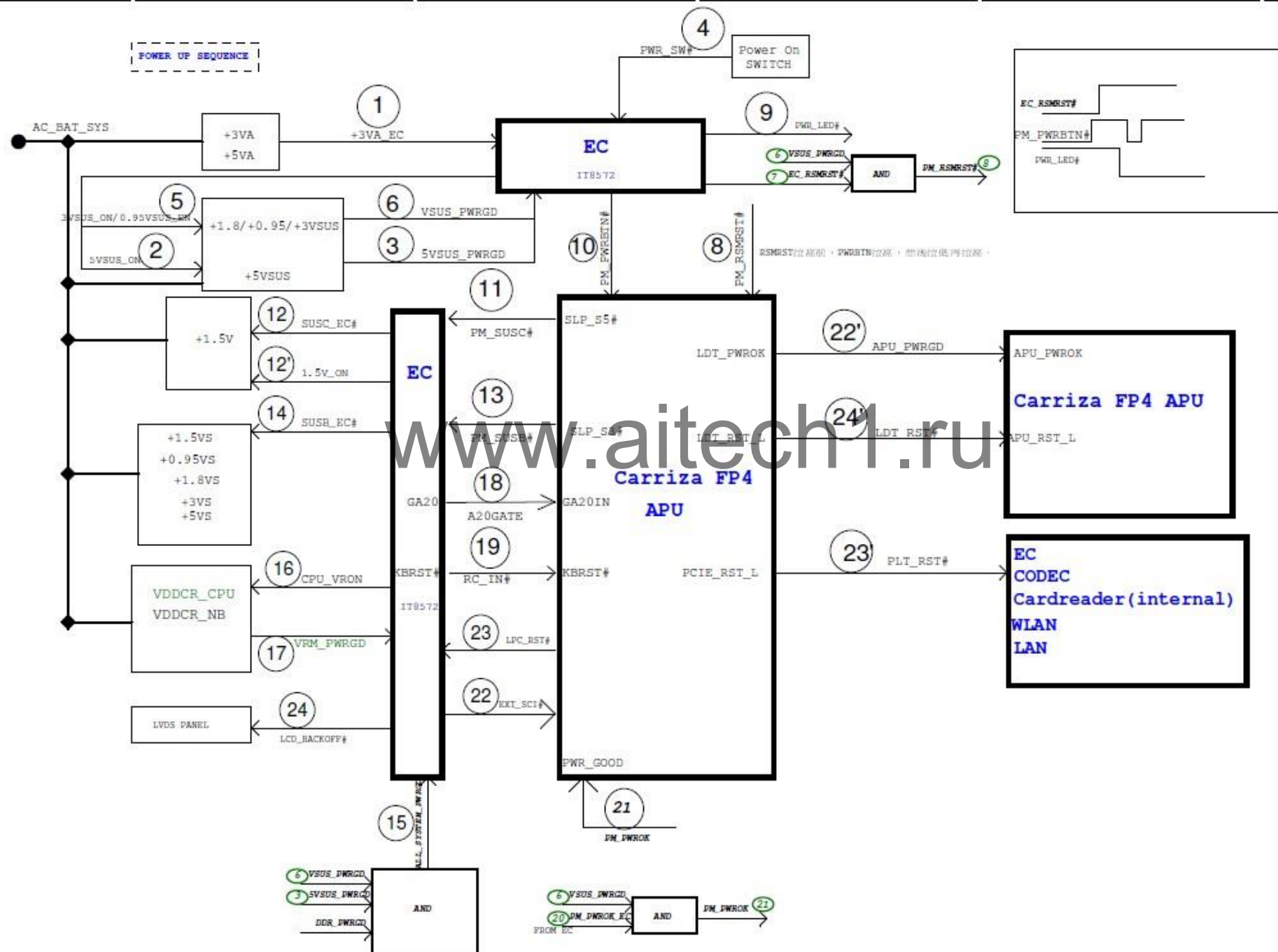
ELP_SA#

LDT_RST#

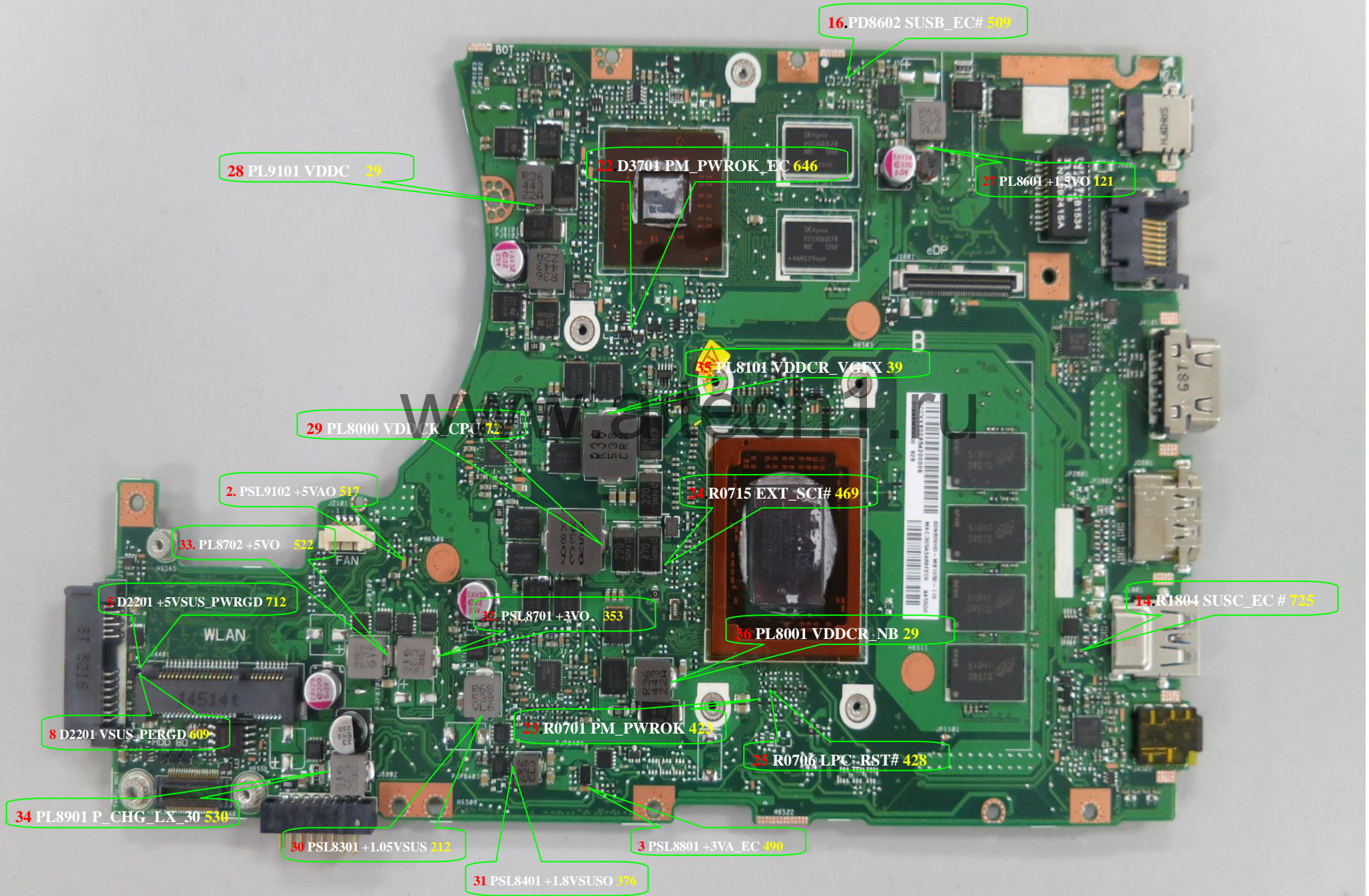
LDT_RST#

Carriza FP4

APU



Signal Measure Point-Bottom



Signal Measure Point-Top

